**Electronic Design Engineer Intern**

**Job Title: Electronic Design Engineer Intern**

**Headcount: 4**

**SBU: S&PS**

**Location: Nanjing**

**Report to: Engineering manager**

**Responsibilities**

* Work with Electronic engineer on test setup and perform engineering test as per test plan.
* Work on data collection and analysis, then report out.
* Take part in prototypes building as per plan. And data collection for evaluate samples.
* Assist design engineer on test system debug and function development
* Support on new product schematic and layout creation and evaluation.
* Regular support based on project needs.

**Qualifications**

* Master degree above, minimum 1 PHD.
* Major in Electronic/Electrical Engineering background or related discipline.
* Strong learning capability and passion on work.
* Good communication skill and team work
* A good working knowledge of analog circuit design, EMC knowledge, mixed signal circuit design.
* Knowledge of Labview and Minitab is preferred
* Good at reading and writing English, fluent oral English will be preferred.
* Good at related software.
* Be able to commit 3 above working days per week from May 2016 to Dec 2016 (longer is better)